



LSG/D.M.X Interface Users Guide

Table of Contents

Using The LSG/DMX Interface	Page 1
XLR Pin Assignments	Page 2
D.M.X. Addressing Example	Page 3
D.M.X. Binary Address Table	Page 4

Specifications

Model : LSG/DMX Interface

Uses DMX 512 Protocol to switch on or off 115 Volt 10 Amp loads.

Size: 190.50mm x 190.50mm x 88.9mm high
7.50" x 7.50" x 3.50" high

Weight: 1.92 Kgs.
4.20 Lbs.

Power: Input : 115 Volts A/C 12 Amps.

Power Output Switched: 10 Amps Maximum switched per auxilliary output
12 Amps Maximum total switched load.

Using The LSG/DMX Interface

The LSG/DMX Interface will allow the user to control the LSG, or any other 115 Volt load that is 10 Amps or less, using a standard 512 DMX signal.

Auxiliary Output # 1 & 2

These outputs are 115 Volts and are switched on and off using the DMX Signal. Each Output is **10 Amps Maximum, with a Maximum combined load of 12 Amps**.

Output 1 is DMX Channel (n). Output 2 is DMX Channel ($n+1$). Output 2 is always the next consecutive higher channel than Output 1.

Main Power Switch

This switch is used to turn on the Main Power. The switch is illuminated when in the **On** position.

Manual Override

This switch is used to manually activate Auxiliary Output #1. When switch is turned on the switch will be illuminated and there will be 115 volts power present at Auxiliary Output #1.

DMX Data In / Out

These two 5-pin connectors receive, and throughput the DMX Data. They are wired per the DMX 512 Protocol, and the pinouts are available on page two.

Green Led

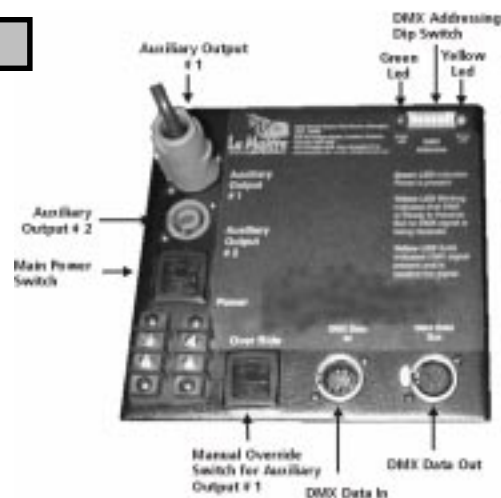
The green led will indicate if power is present at the DMX circuit board.

DMX Addressing Dip Switch

Uses binary addressing to address both DMX channels used on Auxiliary Outputs 1 & 2. DMX setting n is the first output. DMX setting $n+1$ is the second output.

Yellow Led

The yellow LED indicates the presence of DMX signal. A solid Yellow LED indicates that the unit is receiving the DMX signal.



XLR Pin Assignments:

Auxilliary Output 1 & 2

L.= 115 VAC Line
N.= 115 VAC Neutral

D.M.X. Interface

D.M.X. Data In/ Out

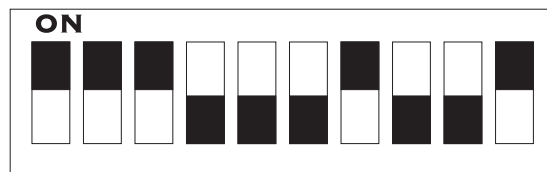
Pin 1. Ground/Return
Pin 2. Compliment(-)
Pin 3. DataTrue(+)
Pin 4. Spare
Pin 5. Spare

DMX Addressing Example

The following is a scenario on how to properly address and use the LSG/DMX Interface.

Two fans are required (Fan1, Fan2) to start and stop using a DMX signal. These two fans have a combined Amperage load of less than 12 Amps. The DMX address of the first fan is channel #440.

- 1.) Plug Fan 1 power into Auxilliary Output #1, and plug Fan 2 power into Auxilliary Output #2.
- 2.) Plug main power of the LSG/DMX Interface into a 115 VAC source with a minimum load rating of 12 Amps.
- 3.) Turn on Main power switch of the LSG/DMX Interface as well as the main power to the 2 fans.
- 4.) To address the proper DMX channel on the DMX Addressing Dipswitch, we must refer to the Binary Addressing Table located further in the manual. The address table notes that channel # 440 is entered into the dipswitch as follows: 000111011. the DMX Addressing Dip Switch should look like this:



DMX Addressing Dip Switch

- 5.) When channel #440 is activated at the DMX control source, Fan 1 will turn on. By default, when Channel 441 is activated (440+1=441) at the DMX control source, Fan 2 will turn on.

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel (<i>n</i>)
	1	0	0	0	0	0	0	0	0	1	1
	0	1	0	0	0	0	0	0	0	2	2
	1	1	0	0	0	0	0	0	0	3	3
	0	0	1	0	0	0	0	0	0	4	4
	1	0	1	0	0	0	0	0	0	5	5
	0	1	1	0	0	0	0	0	0	6	6
	1	1	1	0	0	0	0	0	0	7	7
	0	0	0	1	0	0	0	0	0	8	8
	1	0	0	1	0	0	0	0	0	9	9
	0	1	0	1	0	0	0	0	0	10	10
	1	1	0	1	0	0	0	0	0	11	11
	0	0	1	1	0	0	0	0	0	12	12
	1	0	1	1	0	0	0	0	0	13	13
	0	1	1	1	0	0	0	0	0	14	14
	1	1	1	1	0	0	0	0	0	15	15
	0	0	0	0	1	0	0	0	0	16	16
	1	0	0	0	1	0	0	0	0	17	17
	0	1	0	0	1	0	0	0	0	18	18
	1	1	0	0	1	0	0	0	0	19	19
	0	0	1	0	1	0	0	0	0	20	20
	1	0	1	0	1	0	0	0	0	21	21
	0	1	1	0	1	0	0	0	0	22	22
	1	1	1	0	1	0	0	0	0	23	23
	0	0	0	1	1	0	0	0	0	24	24
	1	0	0	1	1	0	0	0	0	25	25
	0	1	0	1	1	0	0	0	0	26	26
	1	1	0	1	1	0	0	0	0	27	27
	0	0	1	1	1	0	0	0	0	28	28
	1	0	1	1	1	0	0	0	0	29	29
	0	1	1	1	1	0	0	0	0	30	30
	1	1	1	1	1	0	0	0	0	31	31
	0	0	0	0	0	1	0	0	0	32	32
	1	0	0	0	0	1	0	0	0	33	33
	0	1	0	0	0	1	0	0	0	34	34

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel (<i>n</i>)
	1	1	0	0	0	1	0	0	0	35	35
	0	0	1	0	0	1	0	0	0	36	36
	1	0	1	0	0	1	0	0	0	37	37
	0	1	1	0	0	1	0	0	0	38	38
	1	1	1	0	0	1	0	0	0	39	39
	0	0	0	1	0	1	0	0	0	40	40
	1	0	0	1	0	1	0	0	0	41	41
	0	1	0	1	0	1	0	0	0	42	42
	1	1	0	1	0	1	0	0	0	43	43
	0	0	1	1	0	1	0	0	0	44	44
	1	0	1	1	0	1	0	0	0	45	45
	0	1	1	1	0	1	0	0	0	46	46
	1	1	1	1	0	1	0	0	0	47	47
	0	0	0	0	1	1	0	0	0	48	48
	1	0	0	0	1	1	0	0	0	49	49
	0	1	0	0	1	1	0	0	0	50	50
	1	1	0	0	1	1	0	0	0	51	51
	0	0	1	0	1	1	0	0	0	52	52
	1	0	1	0	1	1	0	0	0	53	53
	0	1	1	0	1	1	0	0	0	54	54
	1	1	1	0	1	1	0	0	0	55	55
	0	0	0	1	1	1	0	0	0	56	56
	1	0	0	1	1	1	0	0	0	57	57
	0	1	0	1	1	1	0	0	0	58	58
	1	1	0	1	1	1	0	0	0	59	59
	0	0	1	1	1	1	0	0	0	60	60
	1	0	1	1	1	1	0	0	0	61	61
	0	1	1	1	1	1	0	0	0	62	62
	1	1	1	1	1	1	0	0	0	63	63
	0	0	0	0	0	0	0	0	0	64	64
	1	0	0	0	0	0	1	0	0	65	65
	0	1	0	0	0	0	1	0	0	66	66
	1	1	0	0	0	0	1	0	0	67	67
	0	0	1	0	0	0	1	0	0	68	68

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Controll	Flow Channel(<i>n</i>)
	1	0	1	0	0	0	1	0	0	69	69
	0	1	1	0	0	0	1	0	0	70	70
	1	1	1	0	0	0	1	0	0	71	71
	0	0	0	1	0	0	1	0	0	72	72
	1	0	0	1	0	0	1	0	0	73	73
	0	1	0	1	0	0	1	0	0	74	74
	0	1	0	1	0	0	1	0	0	75	75
	0	0	1	1	0	0	1	0	0	76	76
	0	0	1	1	0	0	1	0	0	77	77
	0	1	1	1	0	0	1	0	0	78	78
	0	1	1	1	0	0	1	0	0	79	79
	0	0	0	0	1	0	1	0	0	80	80
	1	0	0	0	1	0	1	0	0	81	81
	0	1	0	0	1	0	1	0	0	82	82
	1	1	0	0	1	0	1	0	0	83	83
	0	0	1	0	1	0	1	0	0	84	84
	1	0	1	0	1	0	1	0	0	85	85
	0	1	1	0	1	0	1	0	0	86	86
	1	1	1	0	1	0	1	0	0	87	87
	0	0	0	1	1	0	1	0	0	88	88
	1	0	0	1	1	0	1	0	0	89	89
	0	1	0	1	1	0	1	0	0	90	90
	1	1	0	1	1	0	1	0	0	91	91
	0	0	1	1	1	0	1	0	0	92	92
	1	0	1	1	1	0	1	0	0	93	93
	0	1	1	1	1	0	1	0	0	94	94
	1	1	1	1	1	0	1	0	0	95	95
	0	0	0	0	0	1	1	0	0	96	96
	1	0	0	0	0	1	1	0	0	97	97
	0	1	0	0	0	1	1	0	0	98	98
	1	1	0	0	0	1	1	0	0	99	99
	0	0	1	0	0	1	1	0	0	100	100
	1	0	1	0	0	1	1	0	0	101	101
	0	1	1	0	0	1	1	0	0	102	102

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	0	0	0	1	0	1	1	0	0	104	104
	1	0	0	1	0	1	1	0	0	105	105
	0	1	0	1	0	1	1	0	0	106	106
	1	1	0	1	0	1	1	0	0	107	107
	0	0	1	1	0	1	1	0	0	108	108
	1	0	1	1	0	1	1	0	0	109	109
	0	1	1	1	0	1	1	0	0	110	110
	1	1	1	1	0	1	1	0	0	111	111
	0	0	0	0	1	1	1	0	0	112	112
	1	0	0	0	1	1	1	0	0	113	113
	0	1	0	0	1	1	1	0	0	114	114
	1	1	0	0	1	1	1	0	0	115	115
	0	0	1	0	1	1	1	0	0	116	116
	1	0	1	0	1	1	1	0	0	117	117
	0	1	1	0	1	1	1	0	0	118	118
	1	1	1	0	1	1	1	0	0	119	119
	0	0	0	1	1	1	1	0	0	120	120
	1	0	0	1	1	1	1	0	0	121	121
	0	1	0	1	1	1	1	0	0	122	122
	1	1	0	1	1	1	1	0	0	123	123
	0	0	1	1	1	1	1	0	0	124	124
	1	0	1	1	1	1	1	0	0	125	125
	0	1	1	1	1	1	1	0	0	126	126
	1	1	1	1	1	1	1	0	0	127	127
	0	0	0	0	0	0	0	1	0	128	128
	1	0	0	0	0	0	0	1	0	129	129
	0	1	0	0	0	0	0	1	0	130	130
	1	1	0	0	0	0	0	1	0	131	131
	0	0	1	0	0	0	0	1	0	132	132
	1	0	1	0	0	0	0	1	0	133	133
	0	1	1	0	0	0	0	1	0	134	134
	1	1	1	0	0	0	0	1	0	135	135
	0	0	0	1	0	0	0	1	0	136	136
	1	0	0	1	0	0	0	1	0	137	137

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	0	1	0	1	0	0	0	1	0	138	138
	1	1	0	1	0	0	0	1	0	139	139
	0	0	1	1	0	0	0	1	0	140	140
	1	0	1	1	0	0	0	1	0	141	141
	0	1	1	1	0	0	0	1	0	142	142
	1	1	1	1	0	0	0	1	0	143	143
	0	0	0	0	1	0	0	1	0	144	144
	1	0	0	0	1	0	0	1	0	145	145
	0	1	0	0	1	0	0	1	0	146	146
	1	1	0	0	1	0	0	1	0	147	147
	0	0	1	0	1	0	0	1	0	148	148
	1	0	1	0	1	0	0	1	0	149	149
	0	1	1	0	1	0	0	1	0	150	150
	1	1	1	0	1	0	0	1	0	151	151
	0	0	0	1	1	0	0	1	0	152	152
	1	0	0	1	1	0	0	1	0	153	153
	0	1	0	1	1	0	0	1	0	154	154
	1	1	0	1	1	0	0	1	0	155	155
	0	0	1	1	1	0	0	1	0	156	156
	1	0	1	1	1	0	0	1	0	157	157
	0	1	1	1	1	0	0	1	0	158	158
	1	1	1	1	1	0	0	1	0	159	159
	0	0	0	0	0	1	0	1	0	160	160
	1	0	0	0	0	1	0	1	0	161	161
	0	1	0	0	0	1	0	1	0	162	162
	1	1	0	0	0	1	0	1	0	163	163
	0	0	1	0	0	1	0	1	0	164	164
	1	0	1	0	0	1	0	1	0	165	165
	0	1	1	0	0	1	0	1	0	166	166
	1	1	1	0	0	1	0	1	0	167	167
	0	0	0	1	0	1	0	1	0	168	168
	1	0	0	1	0	1	0	1	0	169	169
	0	1	0	1	0	1	0	1	0	170	170
	1	1	0	1	0	1	0	1	0	171	171
	0	0	1	1	0	1	0	1	0	172	172

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	1	0	1	1	0	1	0	1	0	173	173
	0	1	1	1	0	1	0	1	0	174	174
	1	1	1	1	0	1	0	1	0	175	175
	0	0	0	0	1	1	0	1	0	176	176
	1	0	0	0	1	1	0	1	0	177	177
	0	1	0	0	1	1	0	1	0	178	178
	1	1	0	0	1	1	0	1	0	179	179
	0	0	1	0	1	1	0	1	0	180	180
	1	0	1	0	1	1	0	1	0	181	181
	0	1	1	0	1	1	0	1	0	182	182
	1	1	1	0	1	1	0	1	0	183	183
	0	0	0	1	1	1	0	1	0	184	184
	1	0	0	1	1	1	0	1	0	185	185
	0	1	0	1	1	1	0	1	0	186	186
	1	1	0	1	1	1	0	1	0	187	187
	0	0	1	1	1	1	0	1	0	188	188
	1	0	1	1	1	1	0	1	0	189	189
	0	1	1	1	1	1	0	1	0	190	190
	1	1	1	1	1	1	0	1	0	191	191
	0	0	0	0	0	0	1	1	0	192	192
	1	0	0	0	0	0	1	1	0	193	193
	0	1	0	0	0	0	1	1	0	194	194
	1	1	0	0	0	0	1	1	0	195	195
	0	0	1	0	0	0	1	1	0	196	196
	1	0	1	0	0	0	1	1	0	197	197
	0	1	1	0	0	0	1	1	0	198	198
	1	1	1	0	0	0	1	1	0	199	199
	0	0	0	1	0	0	1	1	0	200	200
	1	0	0	1	0	0	1	1	0	201	201
	0	1	0	1	0	0	1	1	0	202	202
	1	1	0	1	0	0	1	1	0	203	203
	0	0	1	1	0	0	1	1	0	204	204
	1	0	1	1	0	0	1	1	0	205	205
	0	1	1	1	0	0	1	1	0	206	206

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	1	1	1	1	0	0	1	1	0	207	207
	0	0	0	0	1	0	1	1	0	208	208
	1	0	0	0	1	0	1	1	0	209	209
	0	1	0	0	1	0	1	1	0	210	210
	1	1	0	0	1	0	1	1	0	211	211
	0	0	1	0	1	0	1	1	0	212	212
	1	0	1	0	1	0	1	1	0	213	213
	0	1	1	0	1	0	1	1	0	214	214
	1	1	1	0	1	0	1	1	0	215	215
	0	0	0	1	1	0	1	1	0	216	216
	1	0	0	1	1	0	1	1	0	217	217
	0	1	0	1	1	0	1	1	0	218	218
	1	1	0	1	1	0	1	1	0	219	219
	0	0	1	1	1	0	1	1	0	220	220
	1	0	1	1	1	0	1	1	0	221	221
	0	1	1	1	1	0	1	1	0	222	222
	1	1	1	1	1	0	1	1	0	223	223
	0	0	0	0	0	0	1	1	0	224	224
	1	0	0	0	0	1	1	1	0	225	225
	0	1	0	0	0	1	1	1	0	226	226
	1	1	0	0	0	1	1	1	0	227	227
	0	0	1	0	0	1	1	1	0	228	228
	1	0	1	0	0	1	1	1	0	229	229
	0	1	1	0	0	1	1	1	0	230	230
	1	1	1	0	0	1	1	1	0	231	231
	0	0	0	1	0	1	1	1	0	232	232
	1	0	0	1	0	1	1	1	0	233	233
	0	1	0	1	0	1	1	1	0	234	234
	1	1	0	1	0	1	1	1	0	235	235
	0	0	1	1	0	1	1	1	0	236	236
	1	0	1	1	0	1	1	1	0	237	237
	0	1	1	1	0	1	1	1	0	238	238
	1	1	1	1	0	1	1	1	0	239	239
	0	0	0	0	1	1	1	1	0	240	240
	1	0	0	0	1	1	1	1	0	241	241

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	0	1	0	0	1	1	1	1	0	242	242
	1	1	0	0	1	1	1	1	0	243	243
	0	0	1	0	1	1	1	1	0	244	244
	1	0	1	0	1	1	1	1	0	245	245
	0	1	1	0	1	1	1	1	0	246	246
	1	1	1	0	1	1	1	1	0	247	247
	0	0	0	1	1	1	1	1	0	248	248
	1	0	0	1	1	1	1	1	0	249	249
	0	1	0	1	1	1	1	1	0	250	250
	1	1	0	1	1	1	1	1	0	251	251
	0	0	1	1	1	1	1	1	0	252	252
	1	0	1	1	1	1	1	1	0	253	253
	0	1	1	1	1	1	1	1	0	254	254
	1	1	1	1	1	1	1	1	0	255	255
	0	0	0	0	0	0	0	0	1	256	256
	1	0	0	0	0	0	0	0	1	257	257
	0	1	0	0	0	0	0	0	1	258	258
	1	1	0	0	0	0	0	0	1	259	259
	0	0	1	0	0	0	0	0	1	260	260
	1	0	1	0	0	0	0	0	1	261	261
	0	1	1	0	0	0	0	0	1	262	262
	1	1	1	0	0	0	0	0	1	263	263
	0	0	0	1	0	0	0	0	1	264	264
	1	0	0	1	0	0	0	0	1	265	265
	0	1	0	1	0	0	0	0	1	266	266
	1	1	0	1	0	0	0	0	1	267	267
	0	0	1	1	0	0	0	0	1	268	268
	1	0	1	1	0	0	0	0	1	269	269
	0	1	1	1	0	0	0	0	1	270	270
	1	1	1	1	0	0	0	0	1	271	271
	0	0	0	0	1	0	0	0	1	272	272
	1	0	0	0	1	0	0	0	1	273	273
	0	1	0	0	1	0	0	0	1	274	274
	1	1	0	0	1	0	0	0	1	275	275
	0	0	1	0	1	0	0	0	1	276	276
	1	0	1	0	1	0	0	0	1	277	277
	0	1	1	0	1	0	0	0	1	278	278

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	1	1	1	0	1	0	0	0	1	279	279
	0	0	0	1	1	0	0	0	1	280	280
	1	0	0	1	1	0	0	0	1	281	281
	0	1	0	1	1	0	0	0	1	282	282
	1	1	0	1	1	0	0	0	1	283	283
	0	0	1	1	1	0	0	0	1	284	284
	1	0	1	1	1	0	0	0	1	285	285
	0	1	1	1	1	0	0	0	1	286	286
	1	1	1	1	1	0	0	0	1	287	287
	0	0	0	0	0	1	0	0	1	288	288
	1	0	0	0	0	1	0	0	1	289	289
	0	1	0	0	0	1	0	0	1	290	290
	1	1	0	0	0	1	0	0	1	291	291
	0	0	1	0	0	1	0	0	1	292	292
	1	0	1	0	0	1	0	0	1	293	293
	0	1	1	0	0	1	0	0	1	294	294
	1	1	1	0	0	1	0	0	1	295	295
	0	0	0	1	0	1	0	0	1	296	296
	1	0	0	1	0	1	0	0	1	297	297
	0	1	0	1	0	1	0	0	1	298	298
	1	1	0	1	0	1	0	0	1	299	299
	0	0	1	1	0	1	0	0	1	300	300
	1	0	1	1	0	1	0	0	1	301	301
	0	1	1	1	0	1	0	0	1	302	302
	1	1	1	1	0	1	0	0	1	303	303
	0	0	0	0	1	1	0	0	1	304	304
	1	0	0	0	1	1	0	0	1	305	305
	0	1	0	0	1	1	0	0	1	306	306
	1	1	0	0	1	1	0	0	1	307	307
	0	0	1	0	1	1	0	0	1	308	308
	1	0	1	0	1	1	0	0	1	309	309
	0	1	1	0	1	1	0	0	1	310	310
	1	1	1	0	1	1	0	0	1	311	311
	0	0	0	1	1	1	0	0	1	312	312
	1	0	0	1	1	1	0	0	1	313	313

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	1	0	0	1	1	1	0	0	1	313	313
	0	1	0	1	1	1	0	0	1	314	314
	1	1	0	1	1	1	0	0	1	315	315
	0	0	1	1	1	1	0	0	1	316	316
	1	0	1	1	1	1	0	0	1	317	317
	0	1	1	1	1	1	0	0	1	318	318
	1	1	1	1	1	1	0	0	1	319	319
	0	0	0	0	0	0	1	0	1	320	320
	1	0	0	0	0	0	1	0	1	321	321
	0	1	0	0	0	0	1	0	1	322	322
	1	1	0	0	0	0	1	0	1	323	323
	0	0	1	0	0	0	1	0	1	324	324
	1	0	1	0	0	0	1	0	1	325	325
	0	1	1	0	0	0	1	0	1	326	326
	1	1	1	0	0	0	1	0	1	327	327
	0	0	0	1	0	0	1	0	1	328	328
	1	0	0	1	0	0	1	0	1	329	329
	0	1	0	1	0	0	1	0	1	330	330
	1	1	0	1	0	0	1	0	1	331	331
	0	0	1	1	0	0	1	0	1	332	332
	1	0	1	1	0	0	1	0	1	333	333
	0	1	1	1	0	0	1	0	1	334	334
	1	1	1	1	0	0	1	0	1	335	335
	0	0	0	0	1	0	1	0	1	336	336
	1	0	0	0	1	0	1	0	1	337	337
	0	1	0	0	1	0	1	0	1	338	338
	1	1	0	0	1	0	1	0	1	339	339
	0	0	1	0	1	0	1	0	1	340	340
	1	0	1	0	1	0	1	0	1	341	341
	0	1	1	0	1	0	1	0	1	342	342
	1	1	1	0	1	0	1	0	1	343	343
	0	0	0	1	1	0	1	0	1	344	344
	1	0	0	1	1	0	1	0	1	345	345
	0	1	0	1	1	0	1	0	1	346	346
	1	1	0	1	1	0	1	0	1	347	347

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	0	0	1	1	1	0	1	0	1	348	348
	1	0	1	1	1	0	1	0	1	349	349
	0	1	1	1	1	0	1	0	1	350	350
	1	1	1	1	1	0	1	0	1	351	351
	0	0	0	0	0	1	1	0	1	352	352
	1	0	0	0	0	1	1	0	1	353	353
	0	1	0	0	0	1	1	0	1	354	354
	1	1	0	0	0	1	1	0	1	355	355
	0	0	1	0	0	1	1	0	1	356	356
	1	0	1	0	0	1	1	0	1	357	357
	0	1	1	0	0	1	1	0	1	358	358
	1	1	1	0	0	1	1	0	1	359	359
	0	0	0	1	0	1	1	0	1	360	360
	1	0	0	1	0	1	1	0	1	361	361
	0	1	0	1	0	1	1	0	1	362	362
	1	1	0	1	0	1	1	0	1	363	363
	0	0	1	1	0	1	1	0	1	364	364
	1	0	1	1	0	1	1	0	1	365	365
	0	1	1	1	0	1	1	0	1	366	366
	1	1	1	1	0	1	1	0	1	367	367
	0	0	0	0	1	1	1	0	1	368	368
	1	0	0	0	1	1	1	0	1	369	369
	0	1	0	0	1	1	1	0	1	370	370
	1	1	0	0	1	1	1	0	1	371	371
	0	0	1	0	1	1	1	0	1	372	372
	1	0	1	0	1	1	1	0	1	373	373
	0	1	1	0	1	1	1	0	1	374	374
	1	1	1	0	1	1	1	0	1	375	375
	0	0	0	1	1	1	1	0	1	376	376
	1	0	0	1	1	1	1	0	1	377	377
	0	1	0	1	1	1	1	0	1	378	378
	1	1	0	1	1	1	1	0	1	379	379
	0	0	1	1	1	1	1	0	1	380	380
	1	0	1	1	1	1	1	0	1	381	381

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	0	1	1	1	1	1	1	0	1	382	382
	1	1	1	1	1	1	1	0	1	383	383
	0	0	0	0	0	0	0	1	1	384	384
	1	0	0	0	0	0	0	1	1	385	385
	0	1	0	0	0	0	0	1	1	386	386
	1	1	0	0	0	0	0	1	1	387	387
	0	0	1	0	0	0	0	1	1	388	388
	1	0	1	0	0	0	0	1	1	389	389
	0	1	1	0	0	0	0	1	1	390	390
	1	1	1	0	0	0	0	1	1	391	391
	0	0	0	1	0	0	0	1	1	392	392
	1	0	0	1	0	0	0	1	1	393	393
	0	1	0	1	0	0	0	1	1	394	394
	1	1	0	1	0	0	0	1	1	395	395
	0	0	1	1	0	0	0	1	1	396	396
	1	0	1	1	0	0	0	1	1	397	397
	0	1	1	1	0	0	0	1	1	398	398
	1	1	1	1	0	0	0	1	1	399	399
	0	0	0	0	1	0	0	1	1	400	400
	1	0	0	0	1	0	0	1	1	401	401
	0	1	0	0	1	0	0	1	1	402	402
	1	1	0	0	1	0	0	1	1	403	403
	0	0	1	0	1	0	0	1	1	404	404
	1	0	1	0	1	0	0	1	1	405	405
	0	1	1	0	1	0	0	1	1	406	406
	1	1	1	0	1	0	0	1	1	407	407
	0	0	0	1	1	0	0	1	1	408	408
	1	0	0	1	1	0	0	1	1	409	409
	0	1	0	1	1	0	0	1	1	410	410
	1	1	0	1	1	0	0	1	1	411	411
	0	0	1	1	1	0	0	1	1	412	412
	1	0	1	1	1	0	0	1	1	413	413
	0	1	1	1	1	0	0	1	1	414	414
	1	1	1	1	1	0	0	1	1	415	415

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	0	0	0	0	0	1	0	1	1	416	416
	1	0	0	0	0	1	0	1	1	417	417
	0	1	0	0	0	1	0	1	1	418	418
	1	1	0	0	0	1	0	1	1	419	419
	0	0	1	0	0	1	0	1	1	420	420
	1	0	1	0	0	1	0	1	1	421	421
	0	1	1	0	0	1	0	1	1	422	422
	1	1	1	0	0	1	0	1	1	423	423
	0	0	0	1	0	1	0	1	1	424	424
	1	0	0	1	0	1	0	1	1	425	425
	0	1	0	1	0	1	0	1	1	426	426
	1	1	0	1	0	1	0	1	1	427	427
	0	0	1	1	0	1	0	1	1	428	428
	1	0	1	1	0	1	0	1	1	429	429
	0	1	1	1	0	1	0	1	1	430	430
	1	1	1	1	0	1	0	1	1	431	431
	0	0	0	0	1	1	0	1	1	432	432
	1	0	0	0	1	1	0	1	1	433	433
	0	1	0	0	1	1	0	1	1	434	434
	1	1	0	0	1	1	0	1	1	435	435
	0	0	1	0	1	1	0	1	1	436	436
	1	0	1	0	1	1	0	1	1	437	437
	0	1	1	0	1	1	0	1	1	438	438
	1	1	1	0	1	1	0	1	1	439	439
	0	0	0	1	1	1	0	1	1	440	440
	1	0	0	1	1	1	0	1	1	441	441
	0	1	0	1	1	1	0	1	1	442	442
	1	1	0	1	1	1	0	1	1	443	443
	0	0	1	1	1	1	0	1	1	444	444
	1	0	1	1	1	1	0	1	1	445	445
	0	1	1	1	1	1	0	1	1	446	446
	1	1	1	1	1	1	0	1	1	447	447
	0	0	0	0	0	0	1	1	1	448	448
	1	0	0	0	0	0	1	1	1	449	449

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	0	1	0	0	0	0	1	1	1	450	450
	1	1	0	0	0	0	1	1	1	451	451
	0	0	1	0	0	0	1	1	1	452	452
	1	0	1	0	0	0	1	1	1	453	453
	0	1	1	0	0	0	1	1	1	454	454
	1	1	1	0	0	0	1	1	1	455	455
	0	0	0	1	0	0	1	1	1	456	456
	1	0	0	1	0	0	1	1	1	457	457
	0	1	0	1	0	0	1	1	1	458	458
	1	1	0	1	0	0	1	1	1	459	459
	0	0	1	1	0	0	1	1	1	460	460
	1	0	1	1	0	0	1	1	1	461	461
	0	1	1	1	0	0	1	1	1	462	462
	1	1	1	1	0	0	1	1	1	463	463
	0	0	0	0	1	0	1	1	1	464	464
	1	0	0	0	1	0	1	1	1	465	465
	0	1	0	0	1	0	1	1	1	466	466
	1	1	0	0	1	0	1	1	1	467	467
	0	0	1	0	1	0	1	1	1	468	468
	1	0	1	0	1	0	1	1	1	469	469
	0	1	1	0	1	0	1	1	1	470	470
	1	1	1	0	1	0	1	1	1	471	471
	0	0	0	1	1	0	1	1	1	472	472
	1	0	0	1	1	0	1	1	1	473	473
	0	1	0	1	1	0	1	1	1	474	474
	1	1	0	1	1	0	1	1	1	475	475
	0	0	1	1	1	0	1	1	1	476	476
	1	0	1	1	1	0	1	1	1	477	477
	0	1	1	1	1	0	1	1	1	478	478
	1	1	1	1	1	0	1	1	1	479	479
	0	0	0	0	0	1	1	1	1	480	480
	1	0	0	0	0	1	1	1	1	481	481
	0	1	0	0	0	1	1	1	1	482	482
	1	1	0	0	0	1	1	1	1	483	483
	0	0	1	0	0	1	1	1	1	484	484

Dip Switch No.	1	2	3	4	5	6	7	8	9	DMX Control	Flow Channel(<i>n</i>)
	1	0	1	0	0	1	1	1	1	485	485
	0	1	1	0	0	1	1	1	1	486	486
	1	1	1	0	0	1	1	1	1	487	487
	0	0	0	1	0	1	1	1	1	488	488
	1	0	0	1	0	1	1	1	1	489	489
	0	1	0	1	0	1	1	1	1	490	490
	1	1	0	1	0	1	1	1	1	491	491
	0	0	1	1	0	1	1	1	1	492	492
	1	0	1	1	0	1	1	1	1	493	493
	0	1	1	1	0	1	1	1	1	494	494
	1	1	1	1	0	1	1	1	1	495	495
	0	0	0	0	1	1	1	1	1	496	496
	1	0	0	0	1	1	1	1	1	497	497
	0	1	0	0	1	1	1	1	1	498	498
	1	1	0	0	1	1	1	1	1	499	499
	0	0	1	0	1	1	1	1	1	500	500
	1	0	1	0	1	1	1	1	1	501	501
	0	1	1	0	1	1	1	1	1	502	502
	1	1	1	0	1	1	1	1	1	503	503
	0	0	0	1	1	1	1	1	1	504	504
	1	0	0	1	1	1	1	1	1	505	505
	0	1	0	1	1	1	1	1	1	506	506
	1	1	0	1	1	1	1	1	1	507	507
	0	0	1	1	1	1	1	1	1	508	508
	1	0	1	1	1	1	1	1	1	509	509
	0	1	1	1	1	1	1	1	1	510	510
	1	1	1	1	1	1	1	1	1	511	511